

10. The read system of claim 6 wherein the first and second coupling circuits each comprises:

- a transistor having a base, a collector, and an emitter, wherein the base is connected to the corresponding input signal node, and the collector is connected to a fixed potential;
- a capacitor connected between the emitter of the transistor and the base of the input transistor of the corresponding amplifier circuit; and
- a current generator for directing current through the transistor.

11. In a read system that includes first and second input signal nodes for connection to a magnetoresistive head, that includes first and second input transistors, and that includes first and second collector circuits connected between a fixed potential and the respective first and second input transistors, the improvement comprising:

- a first coupling circuit comprising a first coupling transistor having a base connected to the first input signal node, a collector connected to the fixed potential, and an emitter ac coupled to the second input transistor, and a current generator for directing current through the first coupling transistor; and
- a second coupling circuit comprising a second coupling transistor having a base connected to the second input signal node, a collector connected to the fixed potential, and an emitter ac coupled to the first input transistor, and a current generator for directing current through the second coupling transistor.

12. The read system of claim 11 wherein a first capacitor is connected between the emitter of the first coupling transistor and the second input transistor,

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15. The method of claim 14, wherein the step of amplifying the first signal with the first amplifier circuit comprises:

coupling a first amplifier transistor to the first input signal node;
coupling a first cascode transistor to the first amplifier transistor;
and
coupling a first resistor to the first cascode transistor, the amplified first signal being provided between the first resistor and the first cascode transistor.

16. The method of claim 15, wherein the step of amplifying the second signal with the second amplifier circuit comprises:

coupling a second amplifier transistor to the second input signal node;
coupling a second cascode transistor to the second amplifier transistor; and
coupling a second resistor to the second cascode transistor, the amplified second signal being provided between the second resistor and the second cascode transistor.

17. The method of claim 16, wherein the step of coupling the first capacitor and the first active element between the first input signal node and the second amplifier circuit comprises:

connecting the first capacitor between the first input signal node and the second amplifier transistor; and
connecting the first active element in parallel with the first capacitor between the first input signal node and the second amplifier transistor.

18. The method of claim 17, wherein the step of coupling the second capacitor and the second active element between the second input signal node and the first amplifier circuit comprises:

connecting the second capacitor between the second input signal node and the first amplifier transistor; and
connecting the second active element in parallel with the second capacitor between the second input signal node and the first amplifier transistor.

19. The method of claim 18, wherein the step of connecting the second active element in parallel with the second capacitor between the second input signal node and the first amplifier transistor comprises:

connecting a control element of the second active element to the second input signal node; and
connecting a controlled element of the second active element to a control element of the first amplifier transistor.

20. The method of claim 17, wherein the step of connecting the first active element in parallel with the first capacitor between the first input signal node and the second amplifier transistor comprises:

connecting a control element of the first active element to the first input signal node; and
connecting a controlled element of the first active element to a control element of the second amplifier transistor.

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